



KURUKSHETRA UNIVERSITY KURUKSHETRA
(Established by the State Legislature Act XII of 1956)

**Date-Sheet for the M.Tech. Microelectronics & VLSI Design I & III Semester examination
commencing w.e.f. 05-12-2017.**

(THEORY EXAMINATION)

Time of Examination: 9.30 A.M. to 12.30 P.M. (MORNING SESSION)

Examination Centre : A-694, Kurukshetra-44, Department of Electronics Science

DATE	SUBJECT & PAPER
	I Semester
5-Dec-2017	Paper-MMVD 101: Process Technology for ULSI-I (New)
5-Dec-2017	Paper-MMVD 1.1: Process Technology for ULSI-I (Old)
9-Dec-2017	Paper-MMVD 102: MOSFET Physics and Sub-Micron Device Modeling (New)
9-Dec-2017	Paper-MMVD 1.2: Modeling Techniques for Submicron Devices (Old)
14-Dec-2017	Paper-MMVD 103: VLSI Design (New)
14-Dec-2017	Paper-MMVD 1.3: VLSI Design-system on Silicon (Old)
18-Dec-2017	Paper-MMVD 104: Digital Signal Processing (New)
18-Dec-2017	Paper-MMVD 1.4: Hardware Description Languages (Old)
	III Semester
7-Dec-2017	MMVD-301: Micro Electro Mechanical Systems (MEMS) (New)
7-Dec-2017	MMVD 3.1 (ii) Digital System Testing and Fault Simulation (Old)
12-Dec-2017	MMVD-302: Digital System Testing and Fault Simulation (New)
12-Dec-2017	MMVD 3.2 (i) MEMS and Nanotechnology (Old)
16-Dec-2017	MMVD-303: Nano Science & Technology (New)

- NOTES :**
- 1 On getting a question-paper and before answering it, the candidates must check up and ensure that they have been supplied with the correct question-paper. If the question-paper is not correct they should bring the same to the notice of the Invigilator. Complaints in this regard after the examination shall not be entertained.
 - 2 Electronics gadgets such as pagers, Cell phone and Programmable Calculators etc. are not allowed in the Examination Hall.
 - 3 No extra answer-sheet will be provided.

KURUKSHETRA
2-Nov-2017

CONTROLLER OF EXAMINATIONS