

KURUKSHETRA UNIVERSITY KURUKSHETRA
(Established by the State Legislature Act XII of 1956)
("A+" Grade NAAC Accredited)

Date-sheet for the M.Sc. Electronics Science II Semester and M. Tech Microelectronics & VLSI Design II Semester (For Re-appear Candidates) Examinations commencing w.e.f. 14-09-2020.

(THEORY EXAMINATION)

Time of Examination : 10.00 A.M. (MORNING SESSION)

DATE	SUBJECT AND PAPER
	M.Sc. II Semester
14-Sep-2020	EL- 21: Digital Circuits and System Design
16-Sep-2020	EL-22: Device Models and Circuits Simulation
18-Sep-2020	EL-23: Verilog-Hardware Description Language
21-Sep-2020	EL-24: System Design Using Embedded Processors
24-Sep-2020	EL-25: Opt(i) Foundations of MEMS
24-Sep-2020	EL-25: Opt,(iii) Materials for VLSI
24-Sep-2020	EL-25: Opt.(iv) Device Electronics for VLSI
	M.Tech. Microelectronics & VLSI Design II Semester
14-Sep-2020	MMVD-201: Process Technology for VLSI-II
16-Sep-2020	MMVD-202: Embedded System Design Using 8051
18-Sep-2020	MMVD-203: Analog CMOS Integrated Circuits
21-Sep-2020	MMVD-204: Verilog Hardware Description Language

- NOTES :**
- 1** Before attempting the paper, the candidate will ensure that he/she has downloaded the correct question-paper. No complaint for attempting wrong question-paper by the candidate will be entertained later on.
 - 2** The candidate is required to attempt the question-papers in his/her own handwriting otherwise will be treated as UMC.

KURUKSHETRA

2-Sep-2020

CONTROLLER OF EXAMINATIONS