

KURUKSHETRA UNIVERSITY KURUKSHETRA
(Established by the State Legislature Act XII of 1956)
("A+" Grade NAAC Accredited)

Date-sheet for the M.Sc. Electronics Science II Semester and M. Tech Microelectronics & VLSI Design II Semester Examinations commencing w.e.f. 02-05-2020.

(THEORY EXAMINATION)

Time of Examination : 9.30 A.M. to 12.30 P.M. (MORNING SESSION)

Examination Centre : 560, Department of Electronics Science, K.U. Kurukshetra

DATE	SUBJECT AND PAPER
	M.Sc. II Semester
2-May-2020	EL- 21: Digital Circuits and System Design
6-May-2020	EL-22: Device Models and Circuits Simulation
11-May-2020	EL-23: Verilog-Hardware Description Language
16-May-2020	EL-24: System Design Using Embedded Processors
19-May-2020	EL-25: Opt(i) Foundations of MEMS
19-May-2020	EL-25: Opt,(iii) Materials for VLSI
19-May-2020	EL-25: Opt.(iv) Device Electronics for VLSI
	M.Tech. Microelectronics & VLSI Design II Semester
2-May-2020	MMVD-201: Process Technology for VLSI-II
6-May-2020	MMVD-202: Embedded System Design Using 8051
11-May-2020	MMVD-203: Analog CMOS Integrated Circuits
15-May-2020	MMVD-204: Verilog Hardware Description Language

- NOTES :**
- 1 On getting a question-paper and before answering it, the candidates must check up and ensure that they have been supplied with the correct question-paper. If the question-paper is not correct they should bring the same to the notice of the Invigilator. Complaints in this regard after the examination shall not be entertained.
 - 2 Electronics gadgets such as pagers, Cell phone and Programmable Calculators etc. are not allowed in the Examination Hall.
 - 3 No extra answer-sheet will be provided.

KURUKSHETRA
 9-Mar-2020

CONTROLLER OF EXAMINATIONS