

**KURUKSHETRA UNIVERSITY KURUKSHETRA**  
*(Established by the State Legislature Act XII of 1956)*  
*("A+" Grade NAAC Accredited)*

**Date-Sheet for the M.Sc. Electronics Science & M.Tech Micro & VLSI Design III SEMESTER**  
**(CBCS only) Examinations commencing w.e.f. 21-12-2020.**

**(THEORY EXAMINATION)**

**Time of Examination : 10.00 A.M. (MORNING SESSION)**

<b>DATE</b>	<b>SUBJECT &amp; PAPER</b>
	<b>M.Sc. Electronics Science III Semester</b>
<b>21-Dec-2020</b>	EL-31: MOS Solid State Circuits
<b>26-Dec-2020</b>	EL-32: Semiconductor Materials & Device Characterization
<b>30-Dec-2020</b>	EL-33: Microwave and Optoelectronic Devices
<b>4-Jan-2021</b>	EL-34: Opt.(i) Custom Microelectronics & ASICs
<b>8-Jan-2021</b>	EL-35: Opt.(i) Digital Communication
	<b>M.Tech Micro &amp; VLSI Design 3rd Semester</b>
<b>21-Dec-2020</b>	MMVD-301: Micro Electro Mechanical Systems (MEMS)
<b>26-Dec-2020</b>	MMVD-302: Digital System Testing and Fault Simulation
<b>30-Dec-2020</b>	MMVD-303: Nano Science & Technology

- NOTES :**
- 1 Before attempting the paper, the candidate will ensure that he/she has received the correct question-paper. No complaint for attempting wrong question-paper by the candidate will be entertained later on.**
  - 2 The candidate is required to attempt the question-papers in his/her own handwriting otherwise will be treated as UMC.**

**KURUKSHETRA**

**CONTROLLER OF EXAMINATIONS**

**7-Dec-2020**