KURUKSHETRA UNIVERSITY KURUKSHETRA
(Established by the State Legislature Act XII of 1956)
("A++" Grade NAAC Accredited)

Date-sheet for the M.Sc. Electronics Science II Semester and M. Tech Microelectronics & VLSI Design II Semester Examinations, May/June, 2024

(THEORY EXAMINATION)
Examination Centre : As mentioned in Admit Card
Time of Examination : 9.30 A.M. (MORNING SESSION)

DATE	SUBJECT AND PAPER
	M.Sc. II Semester
9-May-2024	EL-21, Digital Circuits and System Design
13-May-2024	EL-22, Device Models and Circuits Simulation
16-May-2024	EL-23, Verilog-Hardware Description Language
20-May-2024	EL-24, System Design Using Embedded Processors
23-May-2024	EL-25 Opt(i), Foundations of MEMS
23-May-2024	EL-25 Opt(ii), Nano Electronics-Materials & Devices
	M.Tech. Microelectronics & VLSI Design II Semester
9-May-2024	MMVD-201: Process Technology for VLSI-II
13-May-2024	MMVD-202: Embedded System Design Using 8051
16-May-2024	MMVD-203: Analog CMOS Integrated Circuits
20-May-2024	MMVD-204: Verilog Hardware Description Language

NOTES :

1 On getting a question-paper and before answering it, the candidates must check up and ensure that they have been supplied with the correct question-paper. If the question-paper is not correct they should bring the same to the notice of the Invigilator. Complaints in this regard after the examination shall not be entertained.

2 Electronics gadgets such as pagers, Cell phone and Programmable Calculators etc. are not allowed in the Examination Hall.

3 No extra answer-sheet will be provided.

KURUKSHETRA   CONTROLLER OF EXAMINATIONS

4-Apr-2024