

EMNE - 2024

National Conference on

Emerging Micro/Nano-Electronics: Devices, Technology & VLSI Design



Kurukshetra University
Kurukshetra, Haryana

in Collaboration with



Gurugram University
Gurugram, Haryana

Venue:- Department of Electronic Science,
Kurukshetra University, Kurukshetra, Haryana, India
Dates :- 07-08 November, 2024

Conference website:- <http://emne.co.in>

ABOUT THE CONFERENCE

EMNE-2024 is scheduled to be organized on 07 & 08 November 2024 by Department of Electronic Science, Kurukshetra University, Kurukshetra, India in collaboration with Gurugram University, Gurugram, India. This main aim of the conference is to provide a platform to academicians, researchers and technocrats to dwell deep into the most recent innovations, trends, concerns, and practical challenges encountered, as well as solutions adopted, in the fields of Micro/Nanoelectronics Devices, Semiconductor Fabrication Technologies, CMOS VLSI Circuits Design and CAD Tools. There will be Distinguished Lectures, Keynote talks, Invited talks by leading industry leaders and renowned academicians. Original, unpublished research papers are invited for Oral & Poster presentations in the most relevant areas allied to the theme/tracks of the conference.

MICRO/NANO DEVICES

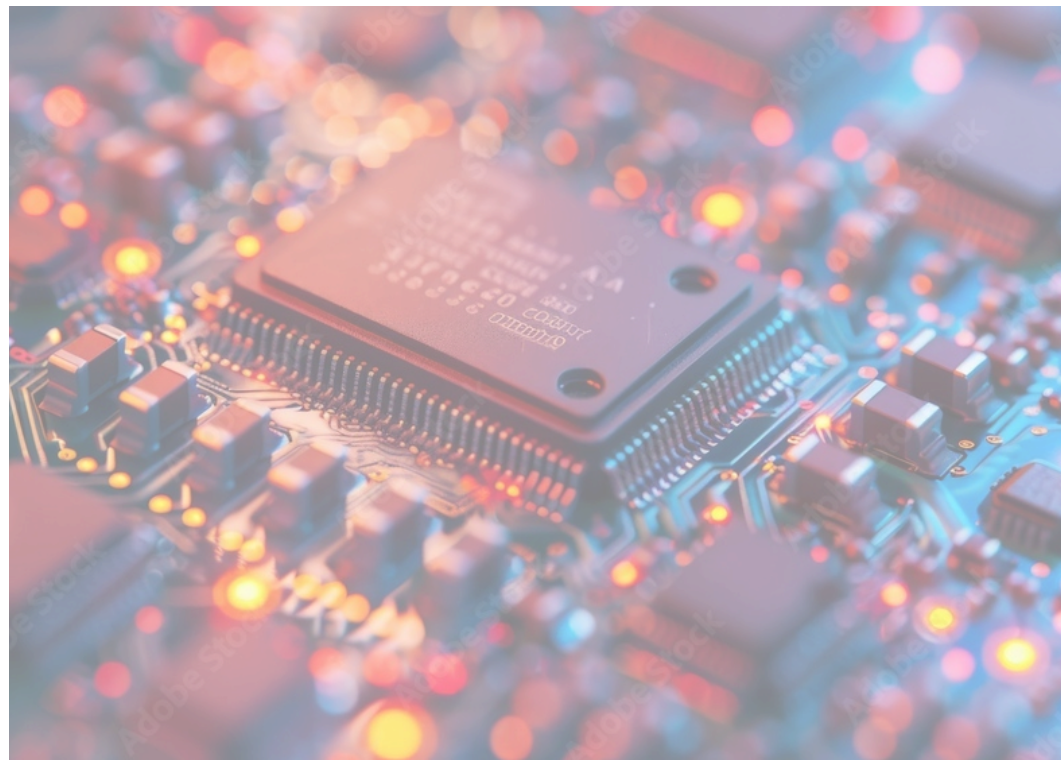
- Nano-CMOS Devices
- FinFET, GAAFET and Quantum devices
- Spintronics
- Emerging Devices and Materials Technology
- Sensors: Materials, Structures and Systems
- Renewable energy: Solar Cell Materials & Structures
- Emerging Memory Technologies
- CMOS Device Modelling
- MEMS Devices

SEMICONDUCTOR FABRICATION TECHNOLOGY

- IC Integration
- Interconnects Technologies
- CMOS & Post CMOS Technologies
- Beyond 2D in packaging and interconnects
- Graphene and other 2D Materials
- Next generation Nanoscale Materials & Nanotechnology
- Advanced Processes & Materials

VLSI DESIGN AND CAD TOOLS

- VLSI Circuits & System Design
- CAD for VLSI
- VLSI Testing & Verification
- IC Reliability
- Low power digital system
- Analog & Mixed Signals
- RF Microelectronics
- Digital CMOS Design



IMPORTANT DATES

Last Date for Paper Submission	01 October 2024
Paper Acceptance Notification	15 October 2024
Last date for author Registration	01 November 2024
Date of Conference	07-08 November 2024

REGISTRATION FEE

On the Spot Registration

Industry	Rs. 4000/-
Faculty Members	Rs. 3000/-
Research Scholars & Students	Rs. 1500/-

Early Bird Registration (before 1 November 2024)

Industry	Rs. 3500/-
Faculty Members	Rs. 2500/-
Research Scholars & Students	Rs. 1000/-

PAPER SUBMISSION

Authors are invited to submit full-length original, unpublished papers along with an abstract of at most 200 words.

Papers will be accepted for oral and poster presentations at the conference. Every accepted paper must have at least one author registered for the conference.

Paper Submission will be done through Microsoft CMT Paper submission system

CONFERENCE PROCEEDINGS

The proceedings of the conference will be published by a reputed publisher after peer reviewing of the full length papers.

Patrons

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Electronic Science Department, Kurukshetra University, Kurukshetra, Haryana

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Rahul Gupta
UIET, Kurukshetra University, Kurukshetra, Haryana

Contact Us: Visit Conference Webpage <https://emne.co.in/> or contact us by email at - emne2024@kuk.ac.in

Prof. Mukesh Kumar
Kurukshetra University
Conference Chair
Mob:- +91-9466650645

Prof. Virender Singh
Kurukshetra University
Conference Co-Chair
Mob:- +91-9991003990

Dr. Sumit Choudhary
Gurugram University
Conference Secretary
Mob:- +91-9416278181

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