

KURUKSHETRA UNIVERSITY KURUKSHETRA

(Established by the State Legislature Act XII of 1956)

("A++" Grade NAAC Accredited)

Date-sheet for the M.Sc. Electronics Science II Semester Examinations, May/June, 2025**(THEORY EXAMINATION)****Examination Centre : As mentioned in Admit Card****Time of Examination : 9.30 A.M. (MORNING SESSION)**

DATE	SUBJECT AND PAPER
	M.Sc. II Semester
20-May-2025	EL-21, Digital Circuits and System Design (Non NEP)
22-May-2025	M24-ELE-201: Digital Circuits and System Design (NEP)
22-May-2025	EL-22, Device Models and Circuits Simulation (Non NEP)
26-May-2025	M24-ELE-202: Analog CMOS Integratede Circuits (NEP)
26-May-2025	EL-23, Verilog-Hardware Description Language (Non NEP)
30-May-2025	M24-ELE-203: Verilog Hardware Description Language (NEP)
30-May-2025	EL-24, System Design Using Embedded Processors (Non NEP)
3-Jun-2025	M24-ELE-204: Introduction to Embedded Systems (NEP)
3-Jun-2025	EL-25 Opt(i), Foundations of MEMS (Non NEP)
3-Jun-2025	EL-25 Opt(ii), Nano Electronics-Materials & Devices (Non NEP)
3-Jun-2025	EL-25 Opt(iii), Materials for VLSI (Non NEP)

- NOTES :**
- 1 On getting a question-paper and before answering it, the candidates must check up and ensure that they have been supplied with the correct question-paper. If the question-paper is not correct they should bring the same to the notice of the Invigilator. Complaints in this regard after the examination shall not be entertained.
 - 2 Electronics gadgets such as pagers, Cell phone and Programmable Calculators etc. are not allowed in the Examination Hall.
 - 3 No extra answer-sheet will be provided.

KURUKSHETRA**8-Apr-2025****CONTROLLER OF EXAMINATIONS**